

| Ref # | Hits | Search Query | DBs | Default Operator | Plurals | Time Stamp |
|-------|------|---|---|------------------|---------|------------------|
| L2 | 3 | (ATM and cell\$1 and (memory adj1 array) and read and written and (one adj1 memory adj1 clock adj1 cycle) and mechanism).clm. | US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2005/11/14 10:08 |



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| | | |
|--------------------|--|---|
| #1 | (memory array and atm<IN>metadata) | 6 |
| #2 | (memory and one memory clock cycle<IN>metadata) | 0 |
| #3 | ((memory and array and atm and clock and cycle)<in>metadata) | 0 |
| #4 | ((memory and array and atm and cycle)<in>metadata) | 2 |

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